



Image AF/2827#

PTO/SB/31 (08-03)

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|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------|----------------------------------------------------------|--|----------------------------------|---------------------------|-------------------------------------------------------------------|--|------------------|--------------------------|
| <b>NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES</b>                                                                                                                                                                                                                                                       |                           | Docket Number (Optional)<br>TESSERA 3.0-085 CONT DIV CIP |  |                                  |                           |                                                                   |  |                  |                          |
| <p>In re Application of<br/>Joseph Fjelstad</p> <table border="1"> <tr> <td>Application Number<br/>09/732,821</td> <td>Filed<br/>December 8, 2000</td> </tr> <tr> <td colspan="2">For METHODS FOR MANUFACTURING RESISTORS USING A SACRIFICIAL LAYER</td> </tr> <tr> <td>Art Unit<br/>2827</td> <td>Examiner<br/>A. Chambliss</td> </tr> </table> |                           |                                                          |  | Application Number<br>09/732,821 | Filed<br>December 8, 2000 | For METHODS FOR MANUFACTURING RESISTORS USING A SACRIFICIAL LAYER |  | Art Unit<br>2827 | Examiner<br>A. Chambliss |
| Application Number<br>09/732,821                                                                                                                                                                                                                                                                                                                 | Filed<br>December 8, 2000 |                                                          |  |                                  |                           |                                                                   |  |                  |                          |
| For METHODS FOR MANUFACTURING RESISTORS USING A SACRIFICIAL LAYER                                                                                                                                                                                                                                                                                |                           |                                                          |  |                                  |                           |                                                                   |  |                  |                          |
| Art Unit<br>2827                                                                                                                                                                                                                                                                                                                                 | Examiner<br>A. Chambliss  |                                                          |  |                                  |                           |                                                                   |  |                  |                          |

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner.

The fee for this Notice of Appeal is (37 CFR 1.17(b)) \$ 330.00

Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is: \$ \_\_\_\_\_

A check in the amount of the fee is enclosed.

Payment by credit card. Form PTO-2038 is attached.

The Director has already been authorized to charge fees in this application to a Deposit Account. I have enclosed a duplicate copy of this sheet.

The Director is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 12-1095. I have enclosed a duplicate copy of this sheet.

A petition for an extension of time under 37 CFR 1.136(a) (PTO/SB/22) is enclosed.

I am the

applicant /inventor

assignee of record of the entire interest.  
See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

attorney or agent of record.

Registration number 28,241

attorney or agent acting under 37 CFR 1.34(a).

Registration number if acting under 37 CFR 1.34(a). \_\_\_\_\_

Signature

Marcus J. Millet  
Typed or printed name

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Telephone number

December 18, 2003

Date

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required.  
Submit multiple forms if more than one signature is required, see below\*.

\*Total of 1 forms are submitted.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: MS AF, Commissioner for Patents/P.O. Box 1400, Alexandria, VA 22313-1450, on the date shown below.

Dated: December 18, 2003

Signature: (Marcus J. Millet)